Implementation of a Receiver Modem on FPGA using Xilinx System Generator

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In this talk the speaker will present the FPGA implementation of a receiver modem during his summer internship at Motorola. This implementation is done by using System Generator which maps DSP algorithms onto an FPGA. System Generator is a high-level design tool for Xilinx FPGA that extends the capabilities of Simulink to include generation of an FPGA circuit. Through the use of simple examples, the speaker will demonstrate high level modeling of DSP algorithms and its implementation on an FPGA.